Please type a plus sign (+) inside this box \rightarrow [+] PT@/SB/21 (08-00) Approved for use through 10/31/2002. OMB/0657-00 U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control-number 09/695,414 ECHNOLUGY CENTER Application Number **TRANSMITTAL** Filing Date **FORM** First Named Inventor o be used for all correspondence after initial filing) Hongyong ZHANG et al. Group Art Unit 2813 **Examiner Name** E. Pert Total Number of Pages in This Submission Attorney Docket Number 740756-2218 ENCLOSURES (check all that apply) After Allowance Communication to Group Fee Transmittal Form Assignment Papers (for an Application) ☐ Other Fee Attached Drawing(s) Amendment / Reply Licensing-related Papers Petition ☐ After Final Petition to Convert to a Affidavits/declaration(s) Provisional Application Extension of Time Request Power of Attorney, Revocation Change of Correspondence Express Abandonment Request Address ☐ Information Disclosure Statement Terminal Disclaimer Request for Refund Certified Copy of Priority Document(s) CD, Number of CD(s) Response to Missing Parts/ Remarks The Commissioner is hereby authorized to charge any additional fees Incomplete Application required or credit any overpayments to Deposit Account No. 19-2380 for the above identified docket number. Response to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Eric J. Robinson, Reg. No. 38,285 Nixon Peabody LLP Individual name 8180 Greensboro Drive Suite 800 McLean, VA 22012 Signature August 31, 2001 Date

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In re Patent Application of)	TECHNOLOGY CENTER 2800
Hongyong ZHANG et al.)	
Serial No. 09/695,414) Group Art Unit: 2813	
Filed: October 25, 2000) Examiner: E. Pert	
For: METHOD FOR FORMING)	
SEMICONDUCTOR DEVICE)	

AMENDMENT

Honorable Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office Action mailed May 31, 2001, please amend the above identified application as follows.

IN THE CLAIMS:

Please amend claims 1, 4, 7, 10, 13 and 16 as follows. A marked-up version of the amended claims is attached.

1. (Amended) A method of manufacturing a semiconductor device comprising the steps of:

forming a semiconductor film comprising amorphous silicon on an insulating surface;

forming a crystallization promoting material comprising a metal in contact with said semiconductor film in a chamber; and

crystallizing said semiconductor film in contact with said crystallization promoting material in said chamber,

wherein the step of crystallizing said semiconductor film is carried out successively after the formation of said crystallization promoting material without exposing said semiconductor film and said crystallization promoting material to the air outside said chamber.